Devon Larsen

Los Angeles, CA · devon.larsen@example.com · (213) 555-0187 · linkedin.com/in/example-devon-larsen

My Key Qualifications

- **10 years of electrical hardware design** in aerospace, defense, and medical devices, from Moog control electronics in 2016 through Rocket Lab flight hardware today.
- Owns mixed-signal and power distribution boards through full design flow in **Altium Designer**: released 6 satellite-subsystem boards to production at Rocket Lab with first-pass schematic approval.
- Built verification and qualification plans across **EMI/EMC, vibration, thermal, and TVAC** phases, closing 94 test procedures across 4 hardware releases.
- Hands-on bring-up and debug with **oscilloscopes, logic analyzers, and electronic loads**; cut power-module debug from 5 weeks to 3 isolating SPI and RS-422 timing faults.
- Structured root-cause methods in practice: led **8D investigation** cutting sensor-board rework from 9 to 3 units per lot, plus DFMEA and fishbone analysis at Rocket Lab and Medtronic.
- Mentored **4 early-career engineers** at Rocket Lab, improving design review closure from 10 days to 6.
- Cross-functional work with systems, firmware, mechanical, and production teams on tradeoffs, failure analysis, and release documentation, including ECO process improvements at Moog.

Experience

Senior Electrical Engineer — **Rocket Lab**, space systems company building launch vehicles, satellites, and flight hardware

May 2022 – Present · 4 yrs 6 mos

- Led the design and release of 6 mixed-signal and power distribution boards for satellite subsystems, owning architecture, Altium Designer schematic capture, interface definition, and layout review with systems and manufacturing partners; all 6 released to production with first-pass schematic approval in final design review.
- Drove board bring-up and debug for a power control module with intermittent communication faults, using oscilloscopes, logic analyzers, and targeted firmware test hooks to isolate SPI and RS-422 timing issues, cutting lab debug time from 5 weeks on the prior build to 3.
- Built verification and qualification plans for flight electronics across functional, EMI/EMC, vibration, thermal, and TVAC test phases, defining test coverage and coordinating lab execution; closed 94 test procedures and reports across 4 hardware releases.
- Led root-cause and corrective action work on recurring manufacturing fallout affecting a sensor interface board, running 8D investigations and tightening component derating and workmanship checks with production; reduced board rework from 9 units per lot to 3.
- Mentored 4 early-career electrical engineers on schematic review, test coverage, and release documentation, standardizing review checklists and release package expectations; average design review closure time improved from 10 days to 6.

Sample resume made with Specific Resume · specificresume.com

Electrical Engineer — **Medtronic**, medical device manufacturer

Jan 2019 – Apr 2022 · 3 yrs 3 mos

- Redesigned a mixed-signal acquisition board to fix noise and interface stability issues, updating grounding, ADC front-end filtering, and layout review practices in Altium Designer; bench verification showed analog noise down by 18 mV peak-to-peak.
- Developed verification plans and ran lab bring-up for power and communication functions across 3 product updates, closing 67 verification cases with 100% requirement traceability before design transfer, using oscilloscopes, electronic loads, and scripted data capture in Python.
- Investigated manufacturing returns tied to power sequencing behavior with bench replication, fishbone analysis, and corrective action reviews alongside firmware and manufacturing engineering, cutting repeat failures from 7 units per quarter to 2.
- Designed and reviewed analog, digital, and power circuitry for PCB assemblies in a controlled-release environment, supporting failure analysis and engineering change documentation with quality and manufacturing teams.

Electrical Design Engineer — **Moog Inc.**, aerospace and defense motion control manufacturer

Jan 2016 – Dec 2018 · 2 yrs 11 mos

- Supported bring-up and debug of actuator control electronics for 2 production programs, closing 21 bench issues before qualification testing using oscilloscopes, power supplies, and interface testing on CAN and SPI-connected boards.
- Reduced ECO processing time by 4 business days by standardizing release notes, test attachments, and configuration change summaries with the hardware team.

Education

- **California Polytechnic State University, San Luis Obispo** — Bachelor of Science in Electrical Engineering · 2011 – 2015

Skills

- **Design & Analysis Tools:** Altium Designer, LTspice, Python
- **Test & Lab Equipment:** Oscilloscopes, Spectrum Analyzers, Logic Analyzers
- **Hardware Domains:** Mixed-Signal Circuit Design, Power Electronics, EMI/EMC & Environmental Qualification, DFMEA / FMEA, Root-Cause Analysis, Design-for-Manufacturability